AMX8.8CN: A&E specifications

The Cobranet[™] interface shall provide 8 analog Mic/line input channels and 8 line level output channels, simultaneously with 16 Cobranet[™] input channels and 16 Cobranet[™] output channels. The Cobranet[™] interface shall allow any of the 8 analog and 16 Cobranet[™] input channels to be routed to any of the 8 analog and 16 Cobranet[™] output channels.

The Cobranet[™] interface shall provide the following analog features...

- Each analog input circuit shall employ a fully balanced true differential topology designed to maximize CMRR across all possible input connection methods.
- The input and output connectivity shall be designed to meet AES48 standards for immunity to hum, buzz and SCIN.
- Each input stage shall have a 10kohm input impedance and accommodate signals of up to +24dBu peak, where each input channel gain shall be adjustable from 0dB to +60dB, ie -56dBu (nominal)/+36dbu (peak) to +4dBu (nominal)+24dbu (peak) via a front panel mounted rotary potentiometer
- Each input channel shall have a front panel mounted switch that shall apply +24volts DC to the input in a 'phantom' power connection.
- Each input channel shall have front panel mounted signal (-20dB threshold) and peak LED's.
- Each output channel shall have 600ohm impedance and accommodate signals of up to +24dBu peak.

The Cobranet[™] interface shall provide a front panel indicator for power.

The Cobranet[™] interface shall provide a 100baseTx Cobranet[™] port, allowing for 16 simultaneous audio streams to the network on 4 bundle transmitters and 16 simultaneous audio streams from the network on 8 bundle receivers. The Cobranet port shall use a Neutrik Ethercon connector to ensure a secure connection. The Cobranet[™] audio traffic shall be completely configurable via SNMP, including the following...

- Bundle address and priority
- Bundle Unicast, Multicast and Multi-Unicast settings
- Bundle channel count from 0 to 8 channels
- Cobranet[™] latency of 1.33, 2.67 and 5.33 milliseconds.

The Cobranet[™] interface shall provide >100MIPS of DSP with the following algorithms...

- 8 channels of AGC, HPF, LPF, Parametric EQ (3 band) and Peak Compressor on the analog inputs
- 16 Channel Automatic Microphone Mixer with 2-channel linker device, enabling a 16x8, 30x16 or 41x24 Automixer matrix across 1-2-3 devices respectively.
- Crossover (High)/HPF, LPF, Parametric EQ (8-band) and Peak Compressor on analog output channels 1&2.
- Crossover (Low)/LPF, HPF, Parametric EQ (6-band) and Peak Compressor on analog output channel 3.
- LPF, HPF, Parametric EQ (6-band) and Peak Compressor on analog output channel 4.
- LPF, HPF, Parametric EQ (6-band), Peak Compressor and 4 tap-50mS delay on analog output channels 5-8.

The Cobranet[™] interface shall provide a 100baseTx Ethernet control port. The Ethernet port shall use a Neutrik Ethercon connector to ensure a secure connection. The Ethernet port shall provide control and monitoring of all Cobranet[™] and DSP parameters via TELNET.

The Cobranet[™] interface shall provide a Multidrop RS485 serial port for Cobranet[™] bridging.

The Cobranet[™] interface switched mode power supply shall be capable of operating at any incoming supply from 86 to 265 volts and from 47Hz to 63Hz.

The Cobranet[™] interface shall be 1U (44.5mm) high and 19" wide suitable for mounting in a professional 19" rack.